IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

    Garg *et al.*

Appl. No.: *To be Assigned*

Filed: *Herewith*

For: **RISC Microprocessor Architecture Implementing Multiple Typed Register Sets**

Art Unit: *To be assigned*

Examiner: *To be assigned*

Atty Docket: SP018.C4

Preliminary Amendment

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

    Before examination of the above-referenced patent application, Applicants submit the following amendments for consideration under 37 CFR §1.121.

Amendments

    Kindly enter the following Amendment:

In the Specification

    Page 1, before line 15, please insert the following:

--The present Application is a Continuation Application of U.S. Utility Patent Appl. No.
08/937,361, filed on September 25, 1997, (allowed), which is a continuation of Appl. No.
Now U.S. PAT. 5838986,
08/665,845, filed on June 19, 1996, (patented), which is a continuation of Appl. No. 08/465,239,
Now U.S. PAT. 5682546,
filed June 5, 1995, (patented), which is a continuation of Appl. No. 07/726,773, filed July 8, 1991,
Now U.S. PAT. 5560035,
(patented).--; and
Now U.S. PAT. 5493687,

    please delete lines 18-21 and insert the following: